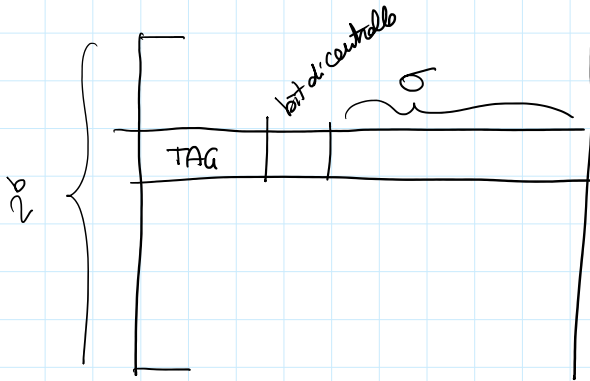


1° modo di indirizzamento della cache : **COMPLETAMENTE ASSOCIATIVO**

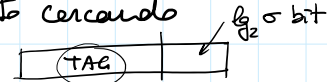


BIT CONTROLLO } Validità = 0 no
1 si

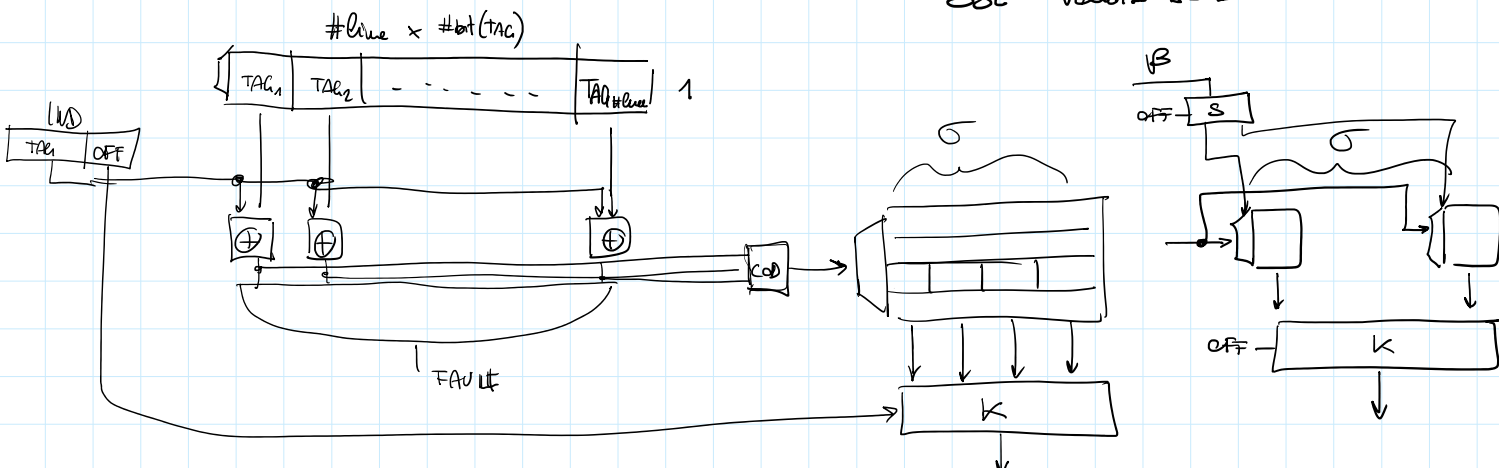
linee x σ
Capacità $2^b \times 2^p / \sigma$

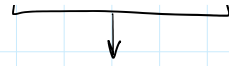
load ↑ ind

① confronto tutti i tag nella cache con la parte iniziale dell'IND che sta cercando $\log_2 \sigma$ bit

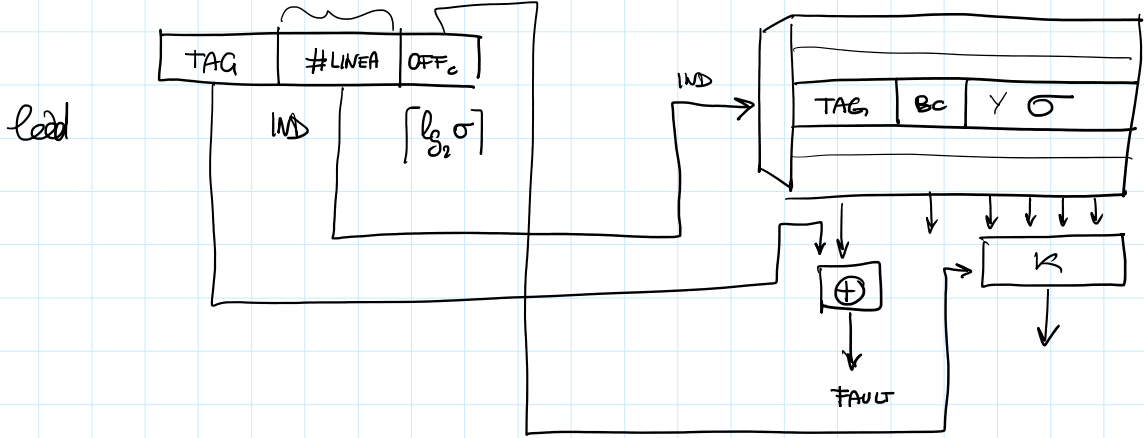


② se un confronto mi dà TRUE di quella linea di cache prendo lo stesso valore dell'offset SSE Validità == 1





INDIRIZZAMENTO DIRETTO

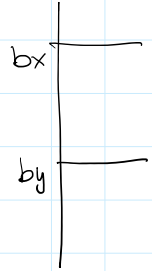


$\sigma = 16$
#linea $2K$

17	11	4
TAG	#linea	OFF

base x	i
	10

$N = 1K$
for()
 $X[i] = f(y[i])$



INDIRIZZAMENTO ASSOCIATIVO SU INSIEMI

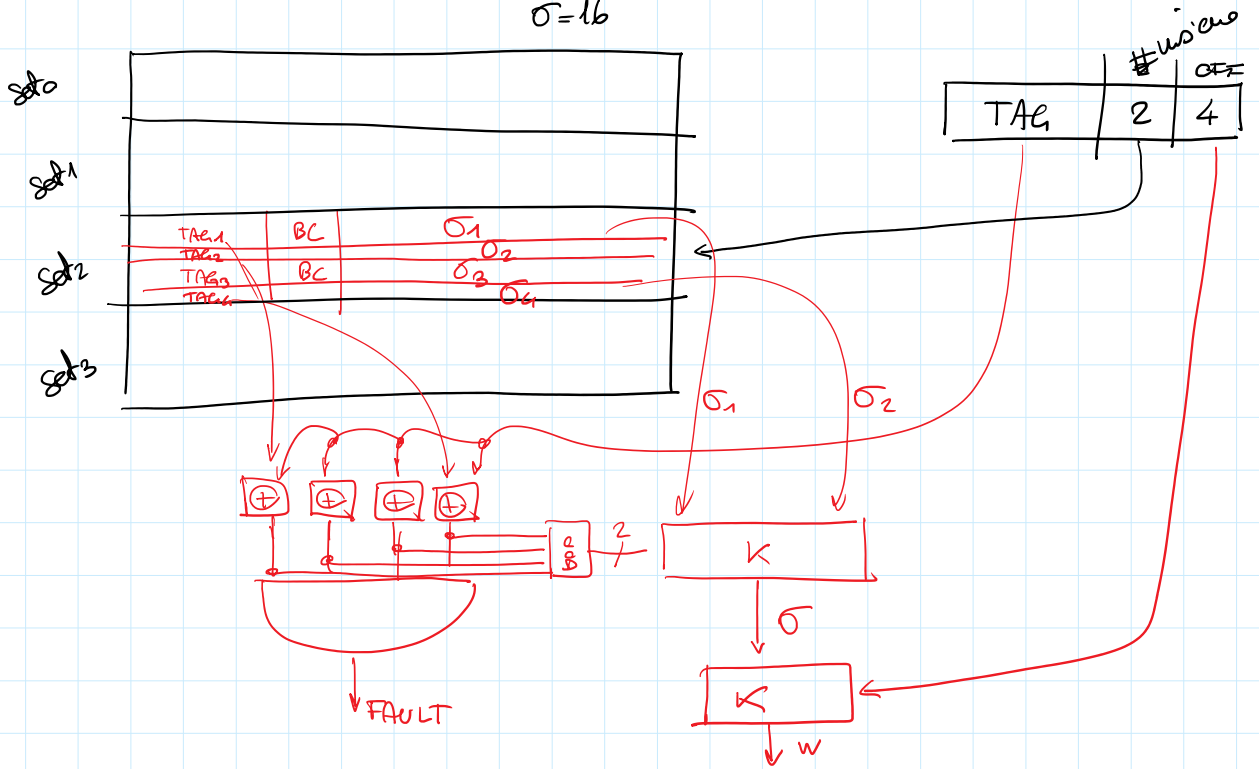
mercoledì 15 novembre 2017 09:50

$\leftarrow (k, 8)$ #vie dello cache Assoc. m insie

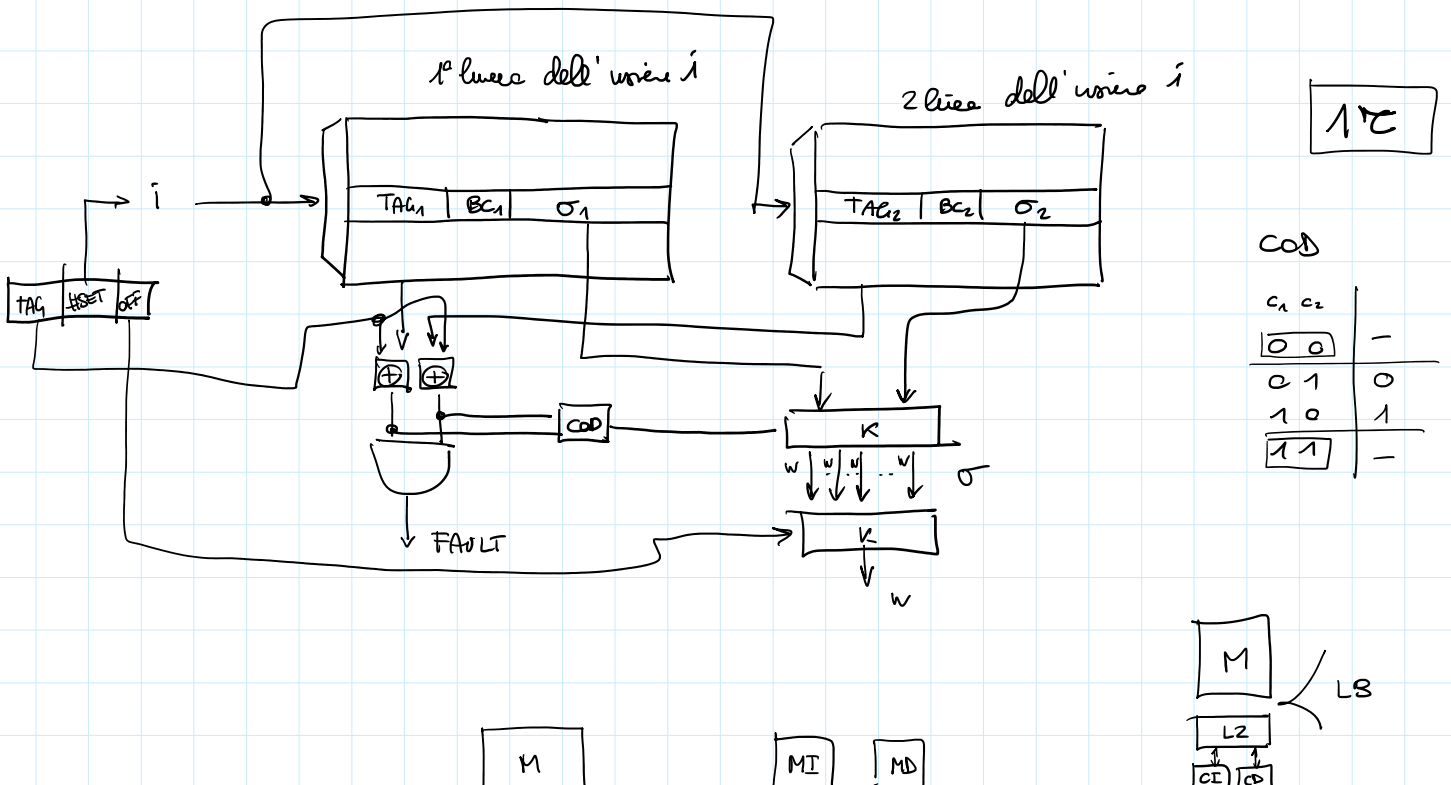
\downarrow
 per le #linee x insieme linee identificate in modo DIRETTO
 \uparrow
 insieme di #linee x insieme linee (identificato in modo DIRETTO)

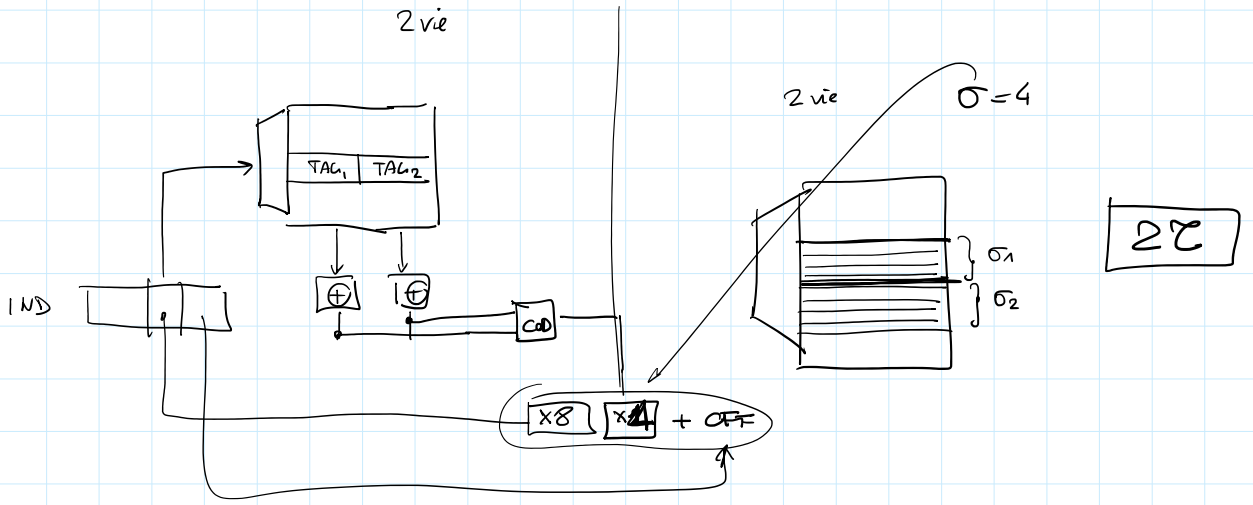
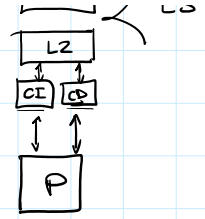
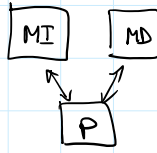
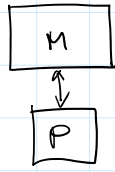
ricerca ASSOCIATIVA

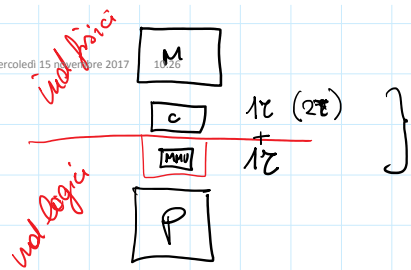
$\sigma = 16$



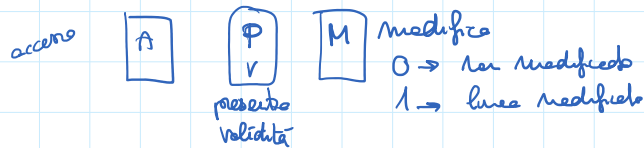
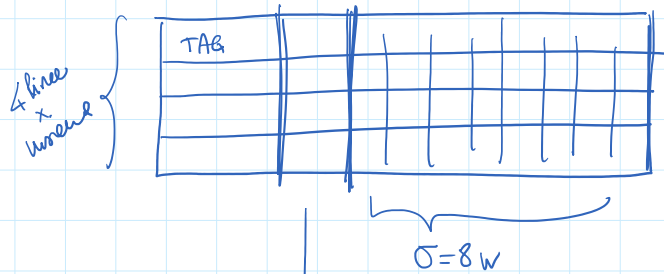
set associativa a 2 vie ($2 \text{ linee} \times \text{insieme}$)
 $\sigma = 4$







cache set associativa



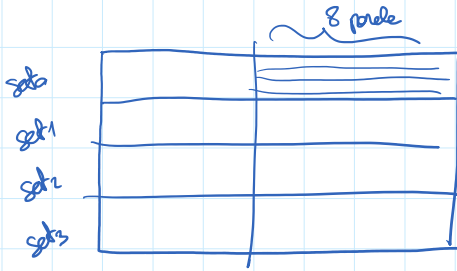
1 \rightarrow TAG è significativo e le σ parole zero nella parte finale della linea
 0 \rightarrow la linea è vuota

periodicamente \Rightarrow 10^i : bit di accesso = \emptyset

l'accesso alle linee i della cache $A_i = 1$

se ho bisogno di una linea "libera" scelgo linea col bit = \emptyset

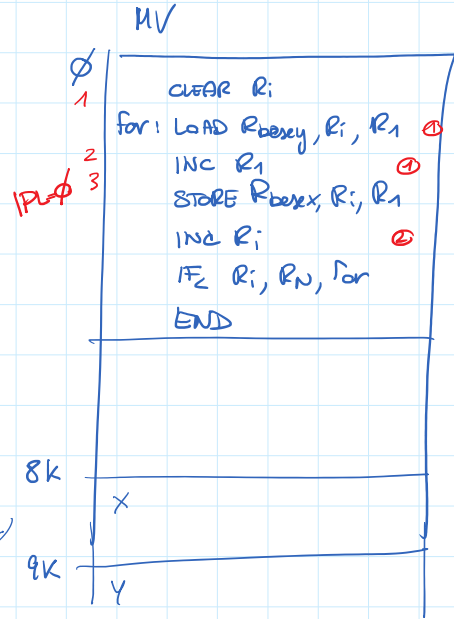
Set associati a 4 vie con $O=8$ #set = 4



27	2	3
TAG	#set	OFF

$N=1024$

```
for(i=0; i<N; i++)
    x[i] = y[i]+1;
```



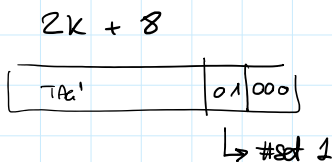
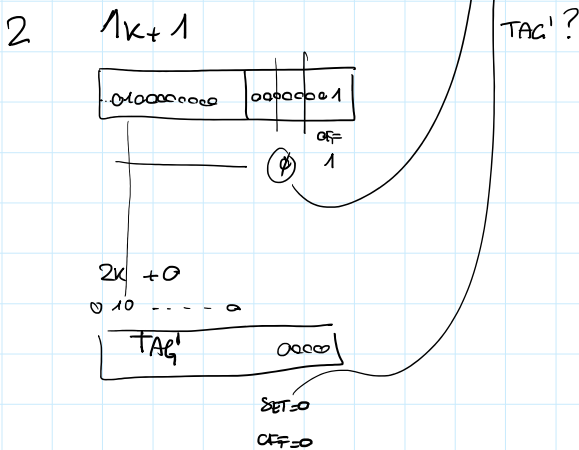
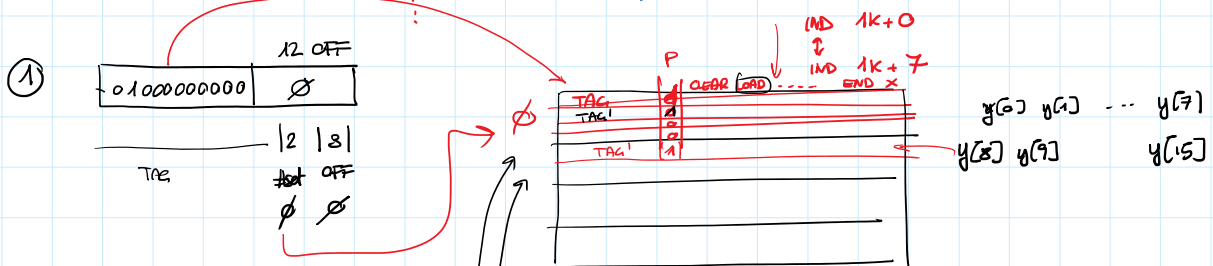
	prima della MMU	dopo MMU	
FETCH LOAD	IND (CLEAR)	0	1k+0 ①
FETCH	IND IND (LOAD ①)	1	1k+1 ②
LOAD	IND (Rbasey + R1)	8k	2k ③
FETCH	IND (INC ②)	2	1k+2 ④
FETCH	IND (STORE)	3	1k+3 ⑤
STORE	IND (Rbasex + R1)	9k	8k+1k ⑥
		4	1k+4 ⑦
		5	1k+5 ⑧
		1	1k+1 ⑨
		8k+1	
		2	
		3	
		9k+1	

IPPL=2

TAB R1

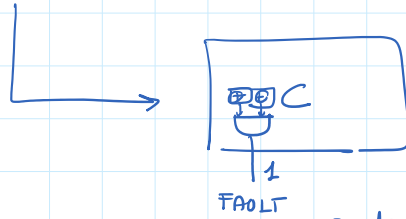
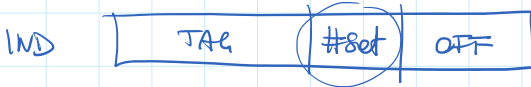
0	1024	1
1	2048	
2	3072	1

pagina = 4k



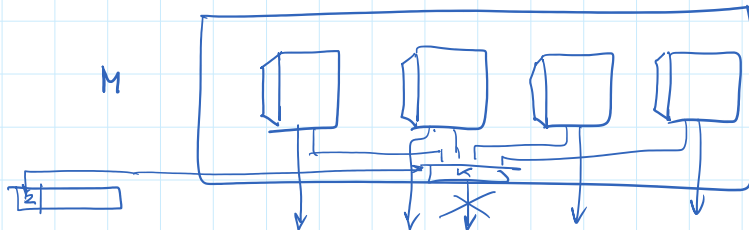


FAULT

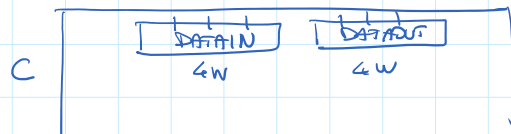


evento interno allo UF "CACHE" !

- 1) selezionare una linea di cache "vittima" #linea 3 nella #set 1
- 2) se $M=1 \Rightarrow$ scritto in M
ordinare un subno di σ parole all'interfaccia di C verso M
- 3) ordinare lo lettura di σ parole da M in #linea 3 di #set 1



m moduli



$$\left(\frac{\sigma}{m} \right) \sum_m + \text{comun.}$$