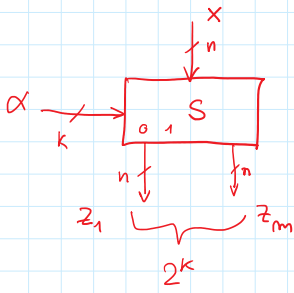


SELEZIONATORE

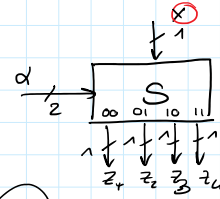


$$\alpha = 0 \begin{cases} z_1 = x \\ z_i (i \neq 1) = 0 \end{cases}$$

$$\alpha = 1 \begin{cases} z_2 = x \\ z_i (i \neq 2) = 0 \end{cases}$$

$$\alpha = 2 \begin{cases} z_3 = x \\ z_i (i \neq 3) = 0 \end{cases}$$

Caso da sintetizzare



PASSO 1 (def)

$8 = 2^3$

$\alpha_1 \alpha_2 x$	z_1	z_2	z_3	z_4
0 0 0	0	0	0	0
0 0 1	1	0	0	0
0 1 0	0	0	0	0
0 1 1	0	1	0	0
1 0 0	0	0	0	0
1 0 1	0	0	1	0
1 1 0	0	0	0	1
1 1 1	0	0	0	1

↑ ↑ ↑ ↑

PASSO 2
TAB VERITA'

PASSO 3

(somma di prodotti x f delle uscite)

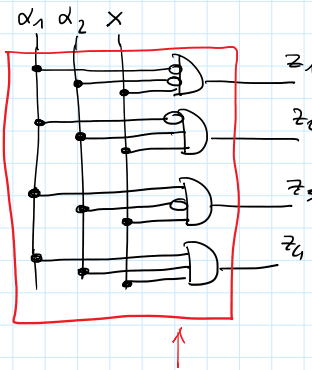
$$z_1 = \bar{\alpha}_1 \bar{\alpha}_2 x$$

$$z_2 = \bar{\alpha}_1 \alpha_2 x$$

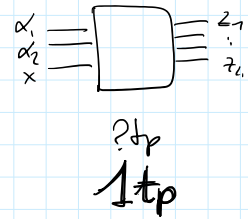
$$z_3 = \alpha_1 \bar{\alpha}_2 x$$

$$z_4 = \alpha_1 \alpha_2 x$$

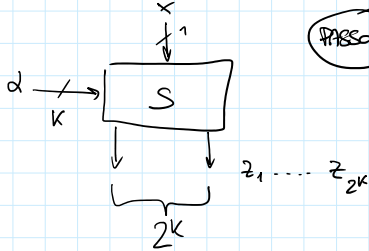
PASSO 4 (porte logiche AND/or/not)



PASSO 5



GENERAZIONE



PASSO 1

$$z_i = \alpha_1 \alpha_2 \dots \alpha_k x$$

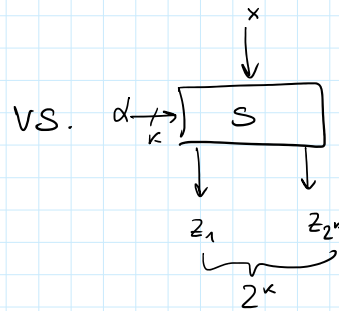
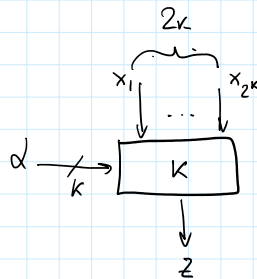
PASSO 2

PASSO 5

soluzione in binari di "i" in corrispondenza di

$$\begin{matrix} 0 & \alpha_j \\ 1 & \alpha_j \end{matrix}$$

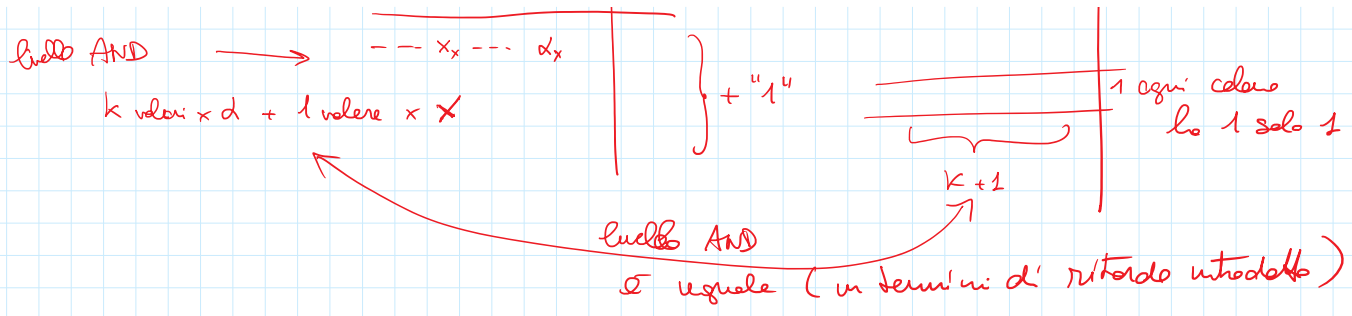
$$\left\lceil \log_2 (k+1) \right\rceil * t_p$$



x_i	α	z
...
x_k	α_k	

α	x	$z_1 \dots z_{2^k}$
...
...

Block AND



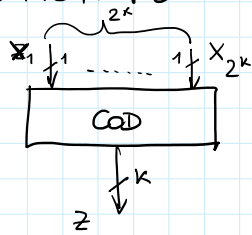
TAB VERITÀ

$k=2$

$x_0 x_1 x_2 x_3$

$z_1 z_2$

CODIFICATORE



z è la configurazione di bit che mi dice qual'è il numero dell'ingresso a 1

unico

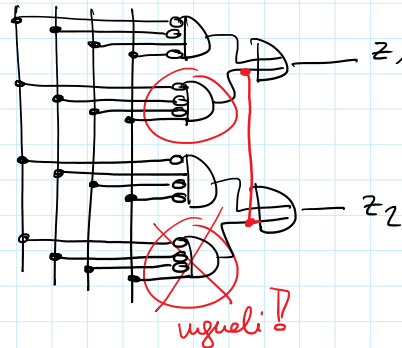
so
le righe
che

x_0	x_1	x_2	x_3	z_1	z_2
1	0	0	0	0	0
0	1	0	0	0	1
0	0	1	0	1	0
0	0	0	1	1	1

$$z_1 = \bar{x}_0 \bar{x}_1 x_2 \bar{x}_3 + \bar{x}_0 \bar{x}_1 \bar{x}_2 x_3$$

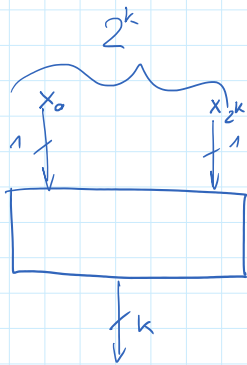
$$z_2 = \bar{x}_0 x_1 \bar{x}_2 \bar{x}_3 + \bar{x}_0 \bar{x}_1 \bar{x}_2 x_3$$

$x_1 x_2 x_3 x_4$



1tp + 1tp
AND OR

2tp



indice del 1° ingresso (da x_0 in poi) →
diverso da ϕ

$k=2$

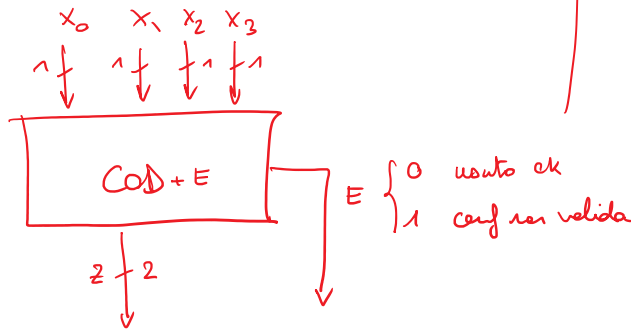
x_0	x_1	x_2	x_3	z_1	z_2
1	-	-	-	0	0
0	1	-	-	0	1
0	0	1	-	1	0
0	0	0	1	1	1

$$z_1 = \bar{x}_0 \bar{x}_1 x_2 + \bar{x}_0 \bar{x}_1 \bar{x}_2 x_3$$

$$z_2 = \bar{x}_0 x_1 + \bar{x}_0 \bar{x}_1 \bar{x}_2 x_3$$

x_0	x_1	x_2	x_3	z_1	z_2
1	0	0	0	0	0
0	1	0	0	0	1
0	0	1	0	1	0
0	0	0	1	1	1

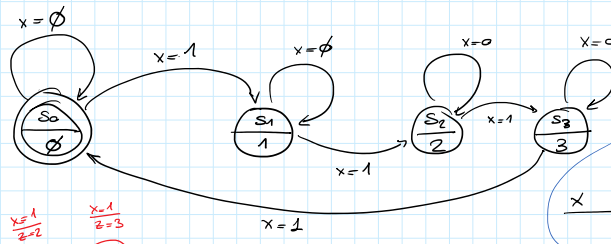
$$z_1 = x_2 + x_3$$



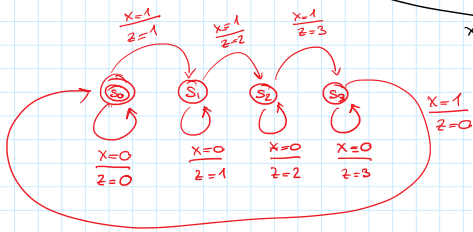
x_0	x_1	x_2	x_3	z_1	z_2	E
0	0	0	0	0	0	1
0	0	0	1	1	1	0
0	0	1	0	1	0	0
0	0	1	1	0	0	1
0	1	0	0	0	1	0
0	1	0	1	0	0	1
0	1	1	0	0	0	1
0	1	1	1	0	0	1
1	0	0	0	0	0	0
1	0	0	1	0	0	1
1	0	1	0	0	0	1
1	0	1	1	0	0	1
1	1	0	0	0	0	1
1	1	0	1	0	0	1
1	1	1	0	0	0	1
1	1	1	1	0	0	1

rete seq che conta il numero di "1" (su un ingresso da 1 bit) modulo 4

Automa di Moore



Mealy

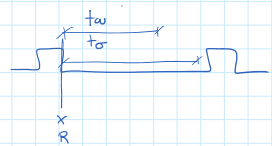


$S_0 = 00$
 $S_1 = 01$
 $S_2 = 10$
 $S_3 = 11$

$w = id$

x	s_1	s_2	$s_1' s_2'$
0	0	0	00
0	0	1	01
0	1	0	10
0	1	1	11
1	0	0	00
1	0	1	01
1	1	0	10
1	1	1	11

$S_1' = X \bar{S}_1 \bar{S}_2 + \bar{X} S_1 \bar{S}_2 + X S_1 S_2 + \bar{X} S_1 S_2$
 $S_1' = X \bar{S}_1 \bar{S}_2 + \bar{X} S_1 \bar{S}_2 + X S_1 S_2 + \bar{X} S_1 S_2$



$t_{wz} = id \Rightarrow \emptyset t_p$

$t_r \Rightarrow 2 t_p$

$C = \max \{ t_w + t_r \} = 2 t_p + 1 t_p$

$= 3 t_p$

4 termini da 3qpm: POTRE AND da 1 POTRE OR da 4 qpm

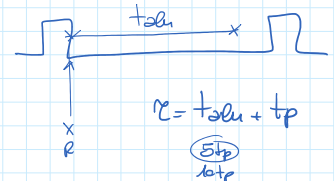
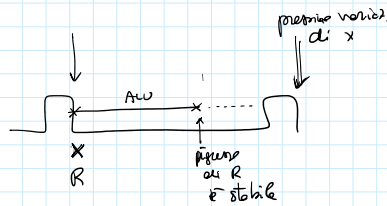
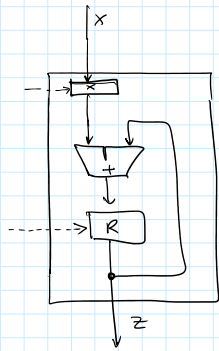
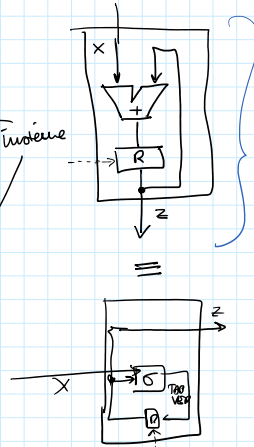
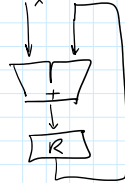
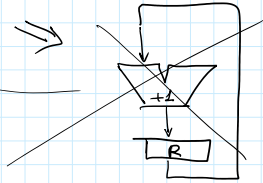
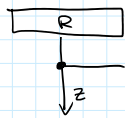
$2 t_p$

Stessa rete seq ma con componenti standard

"uscita è l'identità"

"Stato intero successivo il risultato di un op di incremento di 1"

$S_0 \quad x=1$



$C = t_{wh} + t_p$

5 t_p / 4 t_p

Contatore di "1" nell'ingresso da 1 bit modulo 3

con componenti standard

