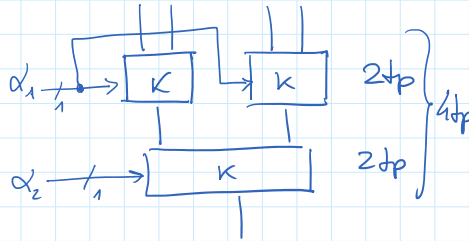


32 righe

| x_{11} | x_{12} | x_{21} | x_{22} | α | z_1 | z_2 |
|----------|----------|----------|----------|----------|-------|-------|
| 0 | 0 | - | - | 0 | 0 | 0 |
| 0 | 1 | - | - | 0 | 0 | 1 |
| 1 | 0 | - | - | 0 | 1 | 0 |
| 1 | 1 | - | - | 0 | 1 | 1 |
| - | - | 0 | 0 | 1 | 0 | 0 |
| - | - | 0 | 1 | 1 | 0 | 1 |
| - | - | 1 | 0 | 1 | 1 | 0 |
| - | - | 1 | 1 | 1 | 1 | 1 |

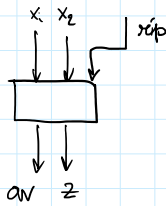


$$z_1 = x_{11} \bar{x}_{12} \bar{\alpha} + x_{11} x_{12} \bar{\alpha} + x_{21} \bar{x}_{22} \alpha + x_{21} x_{22} \alpha$$

$$z_2 =$$

4 termini da 3 vari \Rightarrow AND con 3 ingressi
 \downarrow
 OR con 4 ingressi

Addizione di 2 valori di 1 bit ciascuno

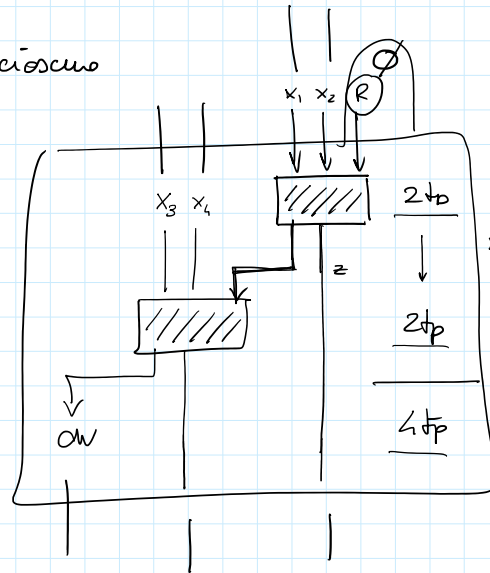


1 1 0 x ← 0
 0 1 1 0
 0 0 1 0

 1 0 0 0

0 0 1 0 ←
 1 2 3 4
 1 2 2 9

 2 4 6 3



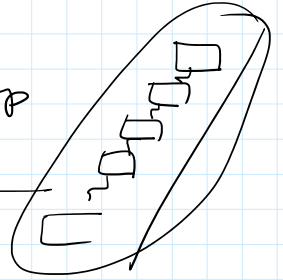
AND da 2 più 4 ingressi \Rightarrow 16 righe
 4 ingressi \Rightarrow 16 righe
 3 uscite
 max 16-1 "1"
 # termini nelle somme (OR)
 2 livelli OR \Rightarrow 8tp
 1tp + 2tp \Rightarrow 8tp

scannatore 2bit + rip → 1row + 1rip

Σ 16bit

16 × 2tp

2tp



2 livelli AND

32 ingressi

2³²

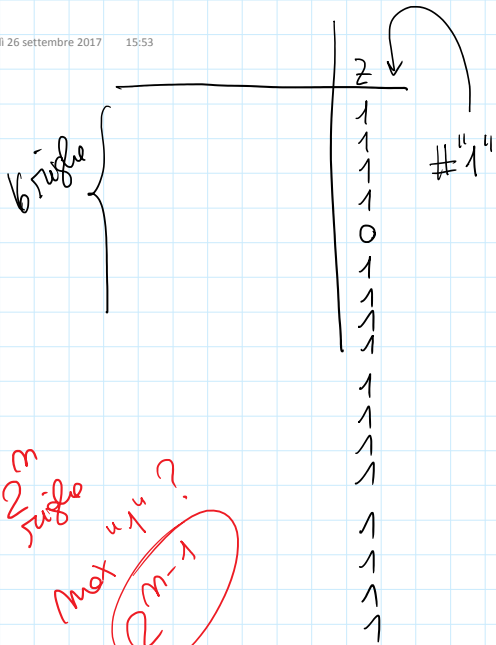
log₂ 2³²

$$2^{32} = \frac{32}{3}$$

livelli OR

~~32/3~~

4



max 15 (16-1)

2^m righe
max "1"
 2^{m-1}

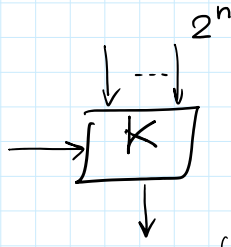
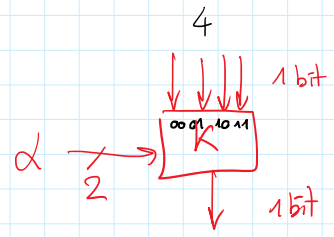
AND 1tp
OR 1tp
NOT 0tp

| x_1 | x_2 | z |
|-------|-------|-----|
| 0 | 0 | 0 |
| 0 | 1 | 1 |
| 1 | 0 | 0 |
| 1 | 1 | 1 |

$z = \bar{x}_1 \bar{x}_2 + \bar{x}_1 x_2 + x_1 x_2$

$z' = x_1 \bar{x}_2$

| x_1 | x_2 | z | z' |
|-------|-------|-----|------|
| 0 | 0 | 0 | 1 |
| 0 | 1 | 0 | 1 |
| 1 | 0 | 1 | 0 |
| 1 | 1 | 0 | 1 |



| $\alpha_1 \alpha_2$ | x_1 | x_2 | x_3 | x_4 | z |
|---------------------|-------|-------|-------|-------|-----|
| 00 | 1 | - | - | - | 1 |
| 01 | - | 1 | - | - | 1 |
| 10 | - | - | 1 | - | 1 |
| 11 | - | - | - | 1 | 1 |

| α | x_i | z |
|----------|---------|-----|
| xxxxx | ---1--- | 1 |
| xxxxx | ---0--- | 0 |

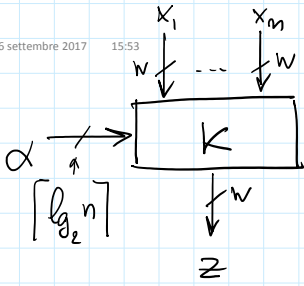
livello AND?

$m+1$ ingressi
 \downarrow
 $\lceil \log_8 (m+1) \rceil$

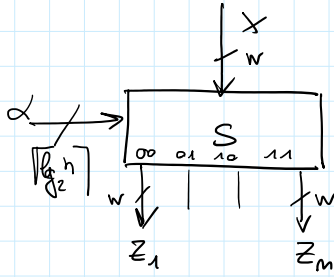
livello OR?

2^m ingressi
 \downarrow
 $\lceil \log_8 (2^m) \rceil$

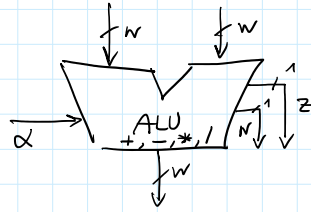
+



Commutatore

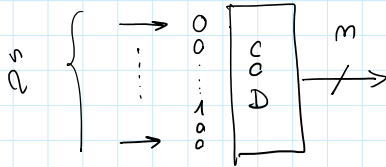


selettore



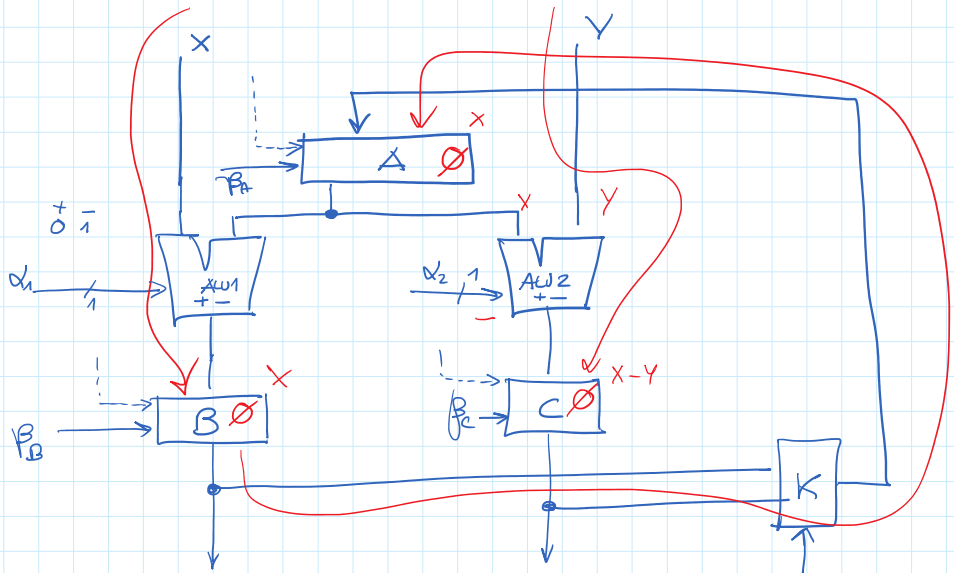
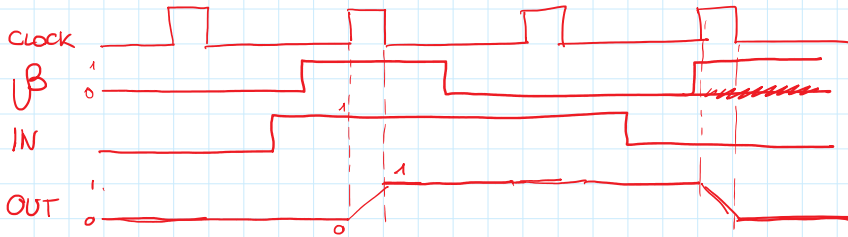
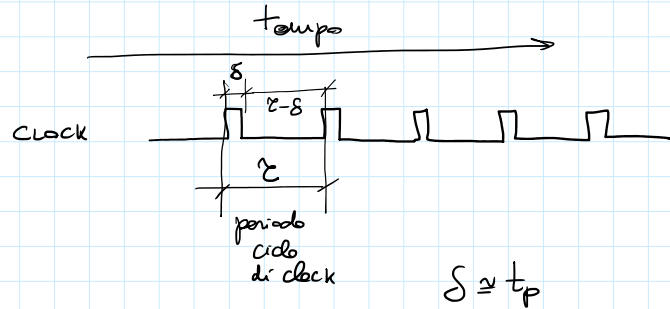
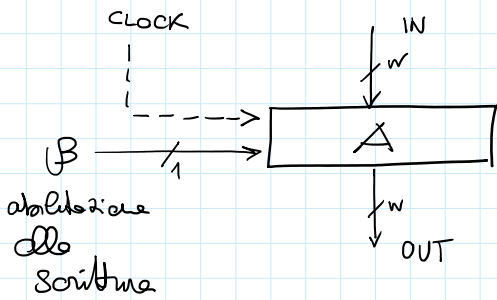
ALU

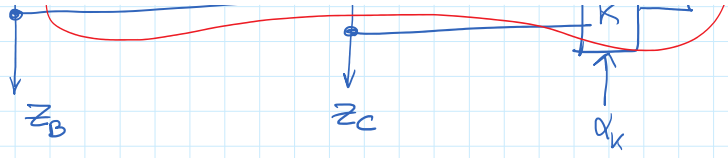
calcolano
funzioni

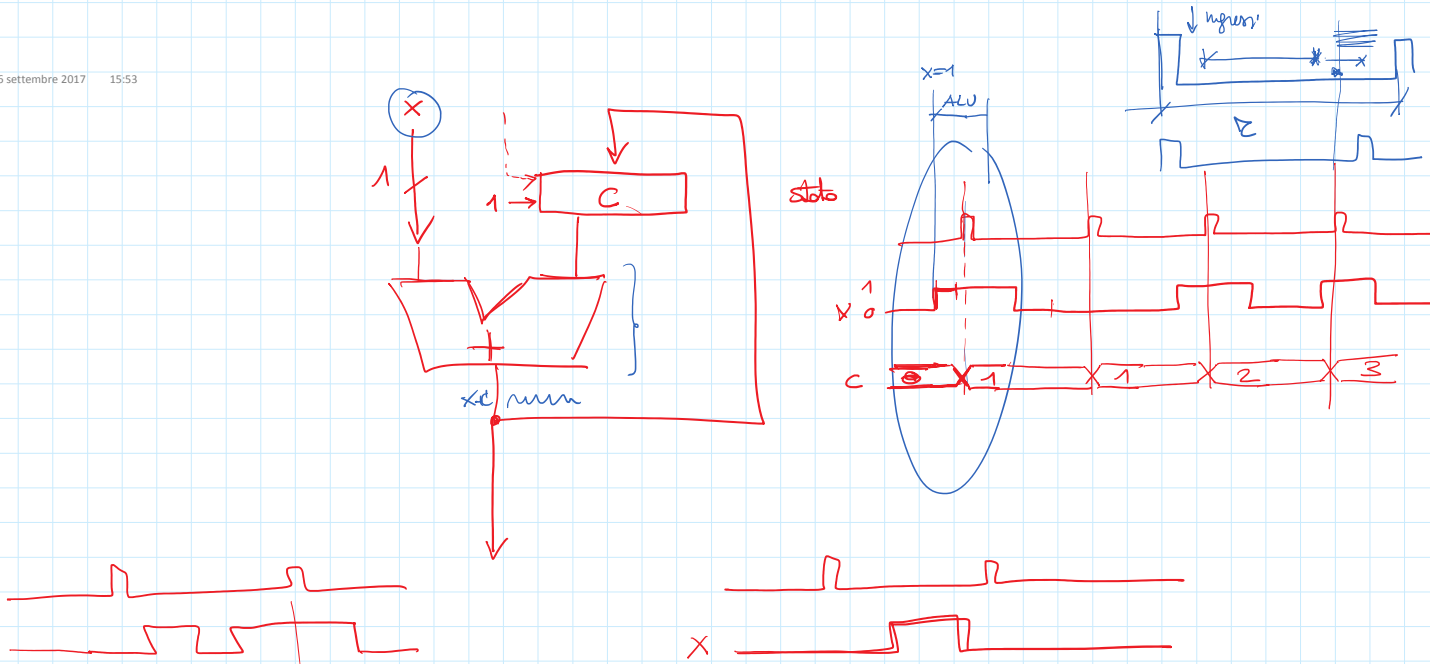


Reti logiche
Combinatorie

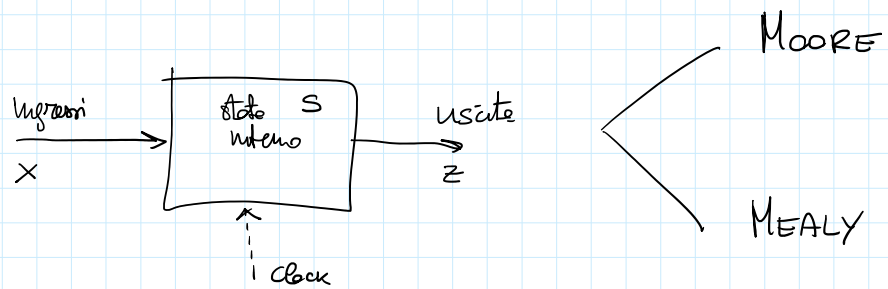
REGISTRI







Reti (logiche) sequenziali



MOORE

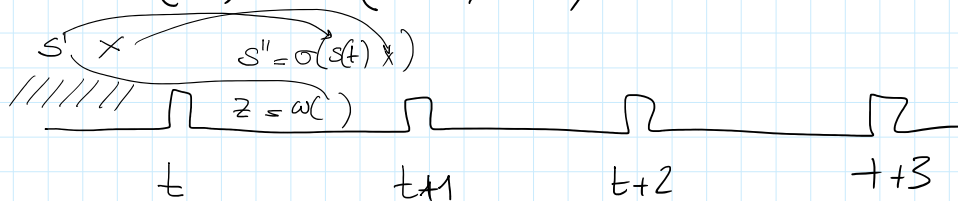
$$z(t) = \omega(s(t))$$

$$s(t+1) = \sigma(s(t), x(t))$$

MEALY

$$z(t) = \omega(x(t), s(t))$$

$$s(t+1) = \sigma(s(t), x(t))$$



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10

martedì 26 settembre 2017 15:54