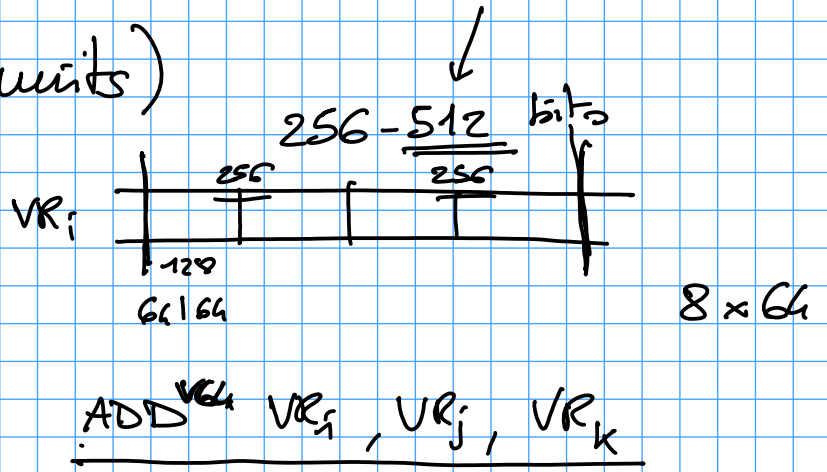


# VECTORIZATION

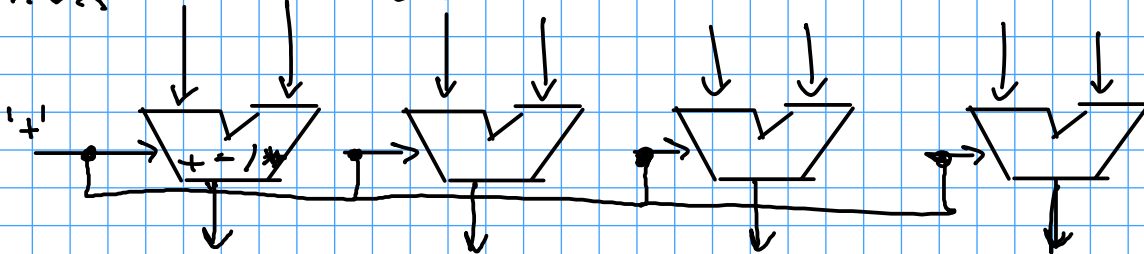
vector units (low units)

vector register file

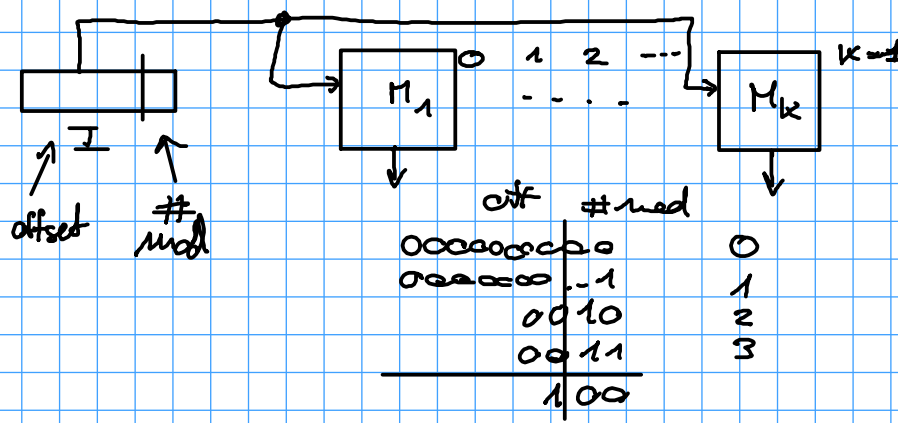
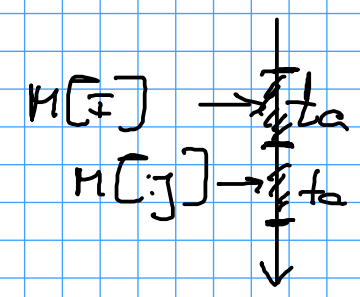
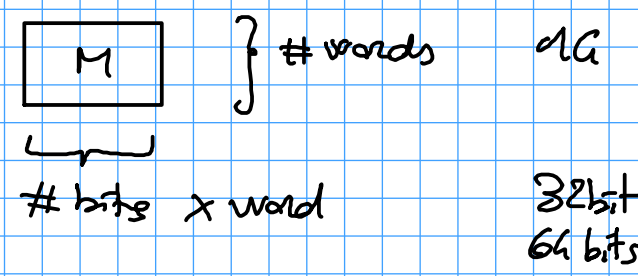
Vector ALUs



MMX MultiMedia Ext  
 ↓  
 SSE Streaming SIMD Ext  
 ↓  
 AVX Advanced Vector Ext

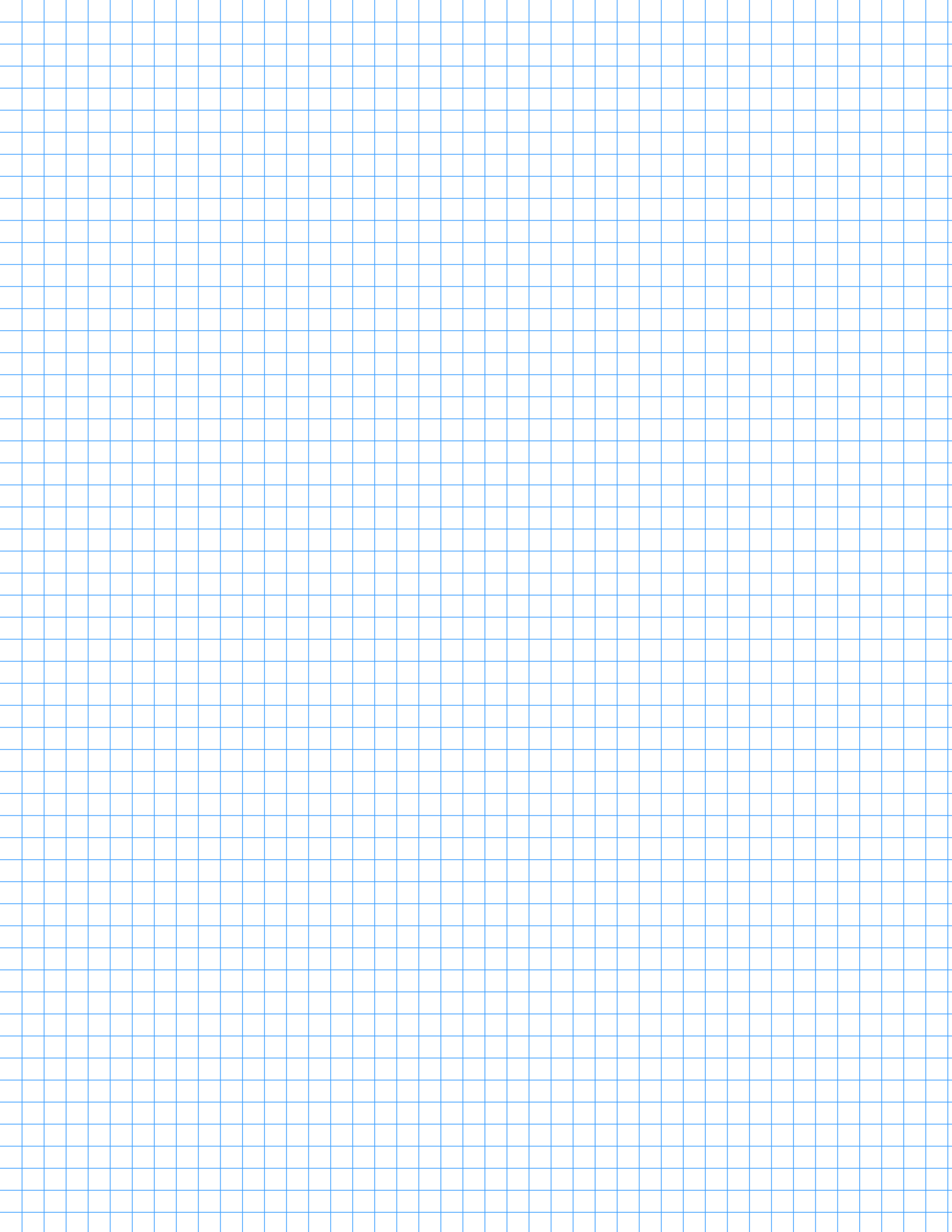


vector LOAD/STORE



$M_i$  1M words of 64b

Load/store a vector register in #items x VR  
 K

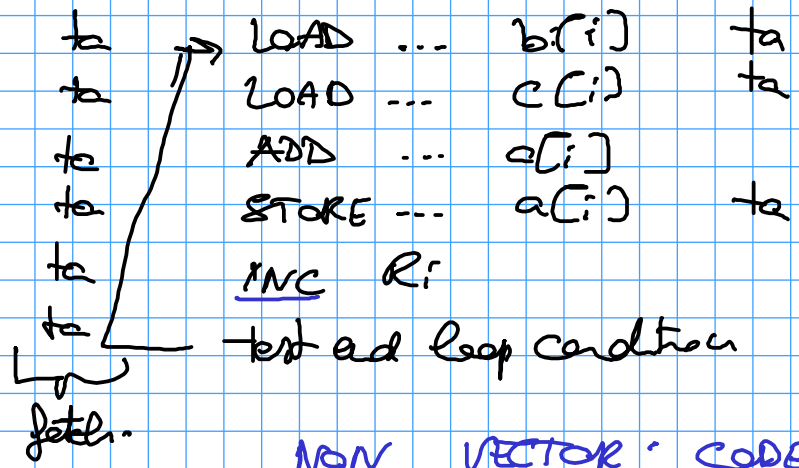


```

for (i=0; i<N; i++)
  a[i] = b[i] + c[i];

```

↑  
int64



VECTOR CODE

NON VECTOR CODES

```

LOADVEC
LOADVEC
ADDVEC
STOREVEC
INC +8
test

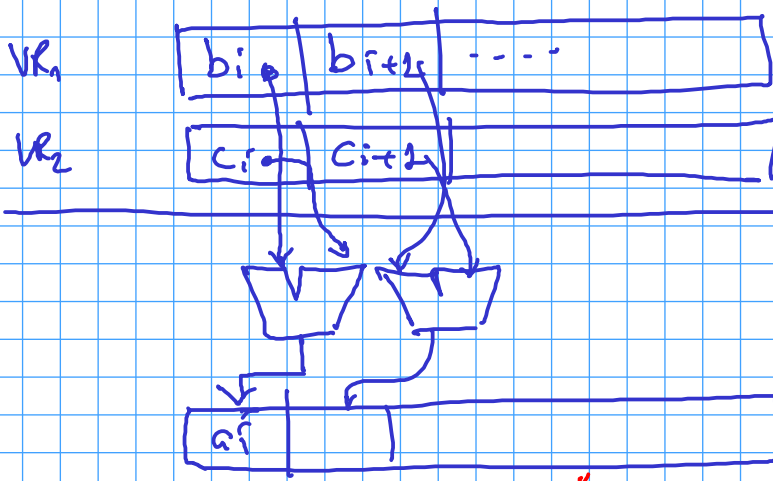
```

```

VR1 ← b[i], b[i+1] ... b[i+7]
VR2

```

9/16 4 iterations of the loop



```

for (i=1; i<N; i++)
  a[i] = a[i-1] + b[i];

```

Loop carried dependencies

dependencies

RAW  
read after write  
 $I_i ; I_{i+1}$

$$R(I_{i+2}) \cap W(I_i) \neq \emptyset$$

& places where I read and write x in →  
are different

WAR  
RAR

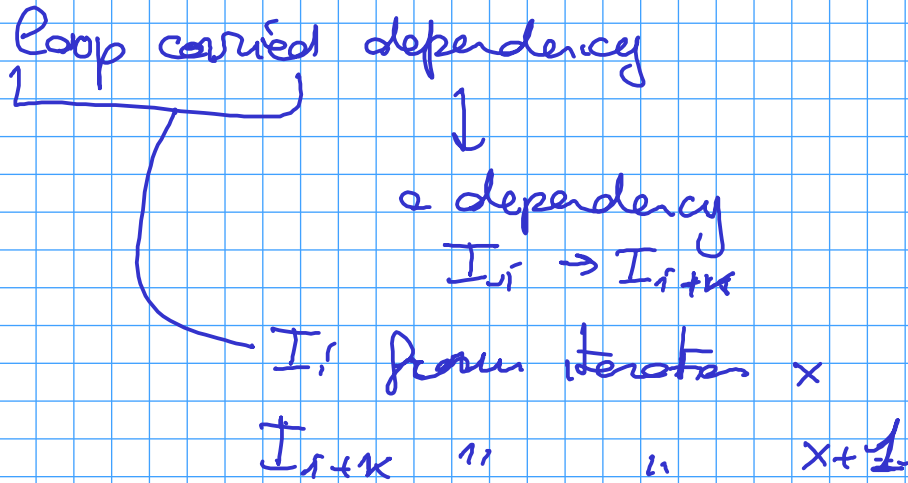
this may be ignored in case of  
synchronous operations (same x)

WAW

RAR  $\Rightarrow$  Multiple reads of the same value

WAW  $\Rightarrow$   $x=1; x=2$

both may be somehow ignored



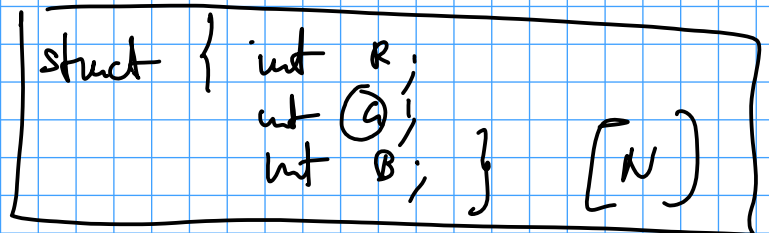
Alignment of data accesses

auto a = new float [n][n];

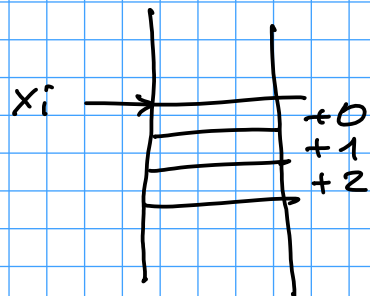
$\rightarrow$  declspec (align (16, 0)) float a [128];



R  
G  
B } color

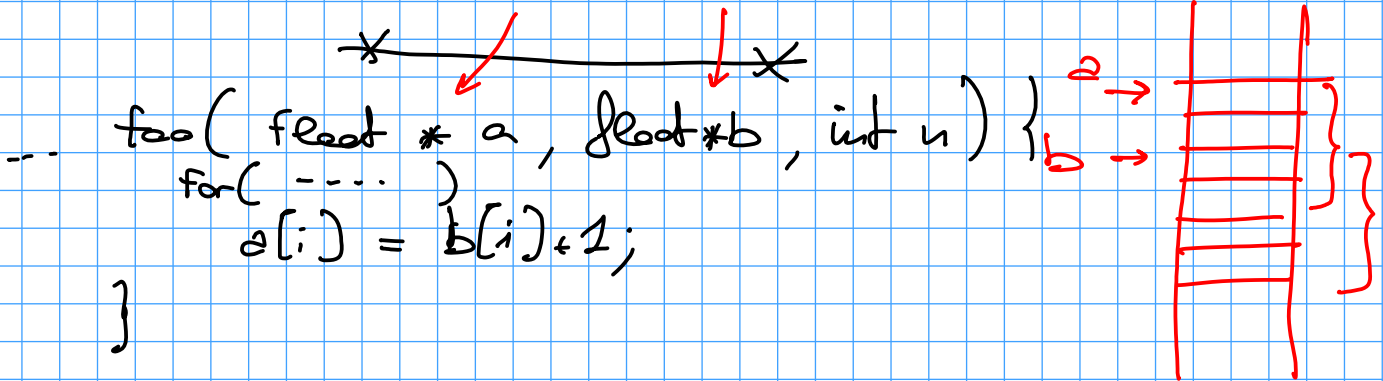


```
for( ... )  
  x[i].R = ...  
  x[i].B = ...
```



```
struct {
  int a[N];
  int b[N];
}
```

x[i].g



restricted float \*a;

~~usage of function calls~~

usage of function calls

```
for(i=0; i<N; i++)
  x[i] = fun(y[i]);
```

# of iterations

for( --- ) ✓  
while( ) } } not ok

default loops with fun calls are not vectorized

unless } fun is inlined  
} fun is one of the intrinsics

no "break" exits per loop  
should not use non-constant loop iteration boundaries

```
#pragma simd  
#pragma ivdep
```